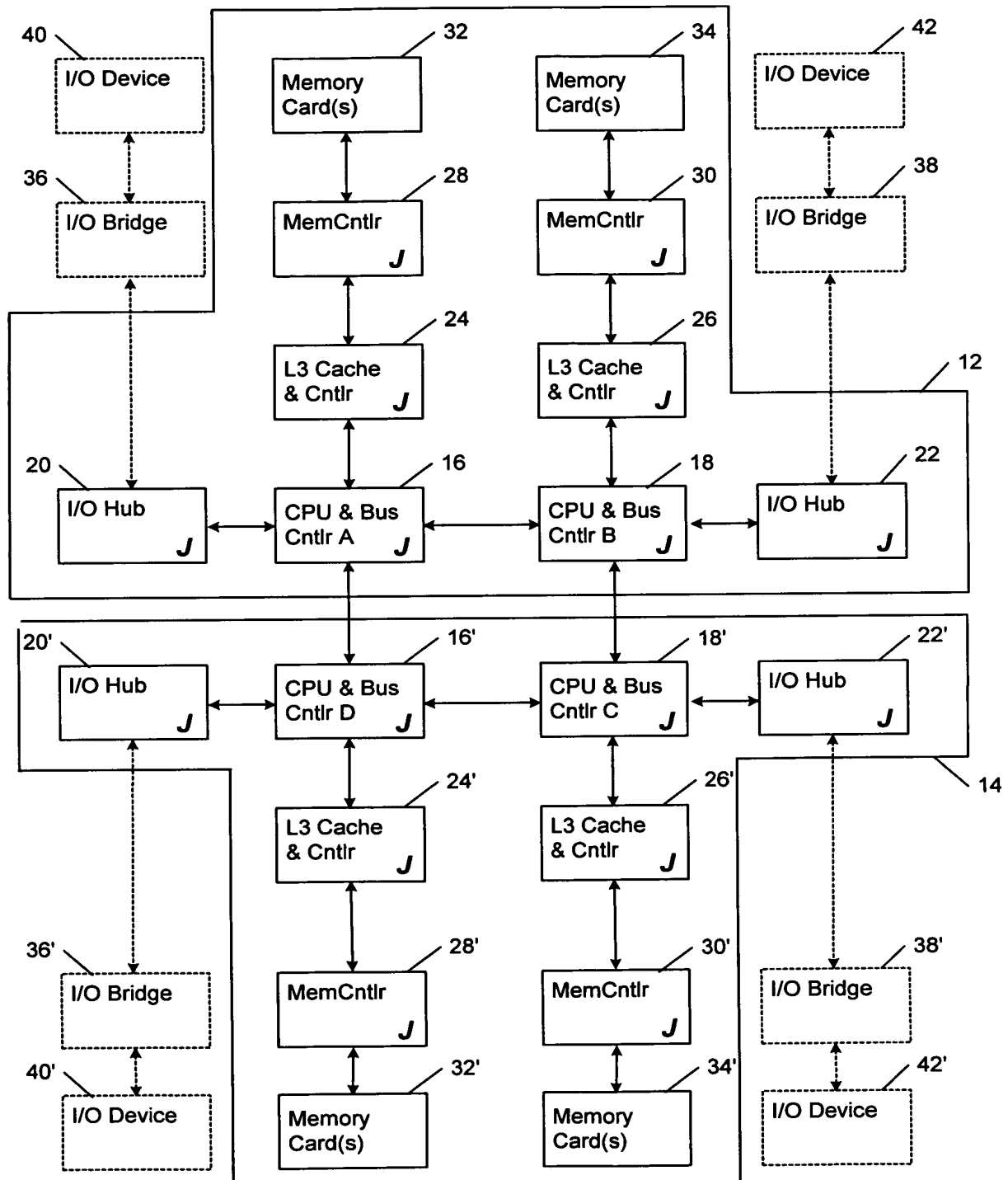


1/7

10



J = chip has serial jtag connection to Service Processor HW
▭ dotted box indicates remote chip or device inaccessible to SP via jtag
↔ dotted line indicates connections to and between remote chips & devices

FIG. 1

50



FIG. 2

3/7

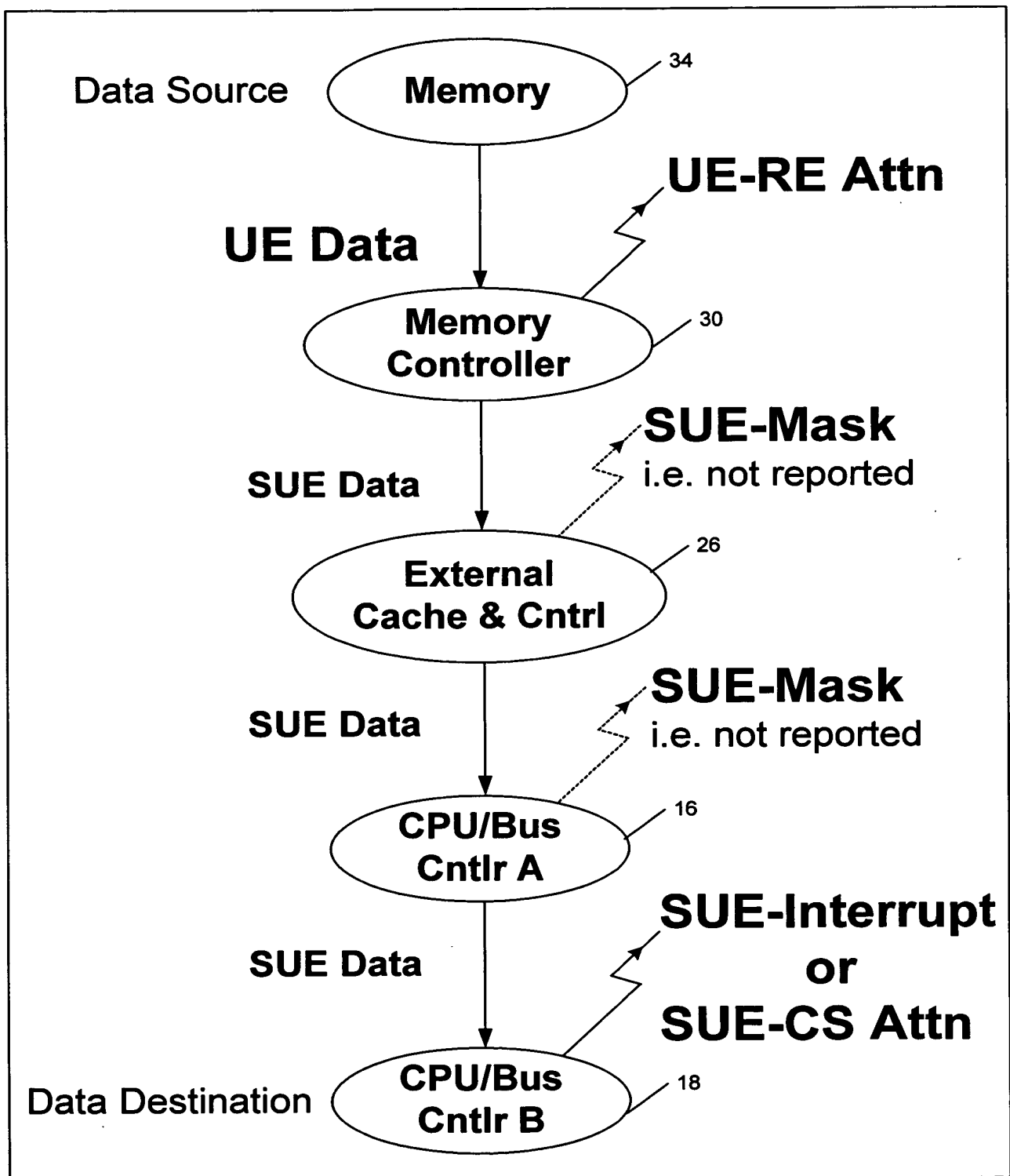


FIG. 3



09/820,461

4/7

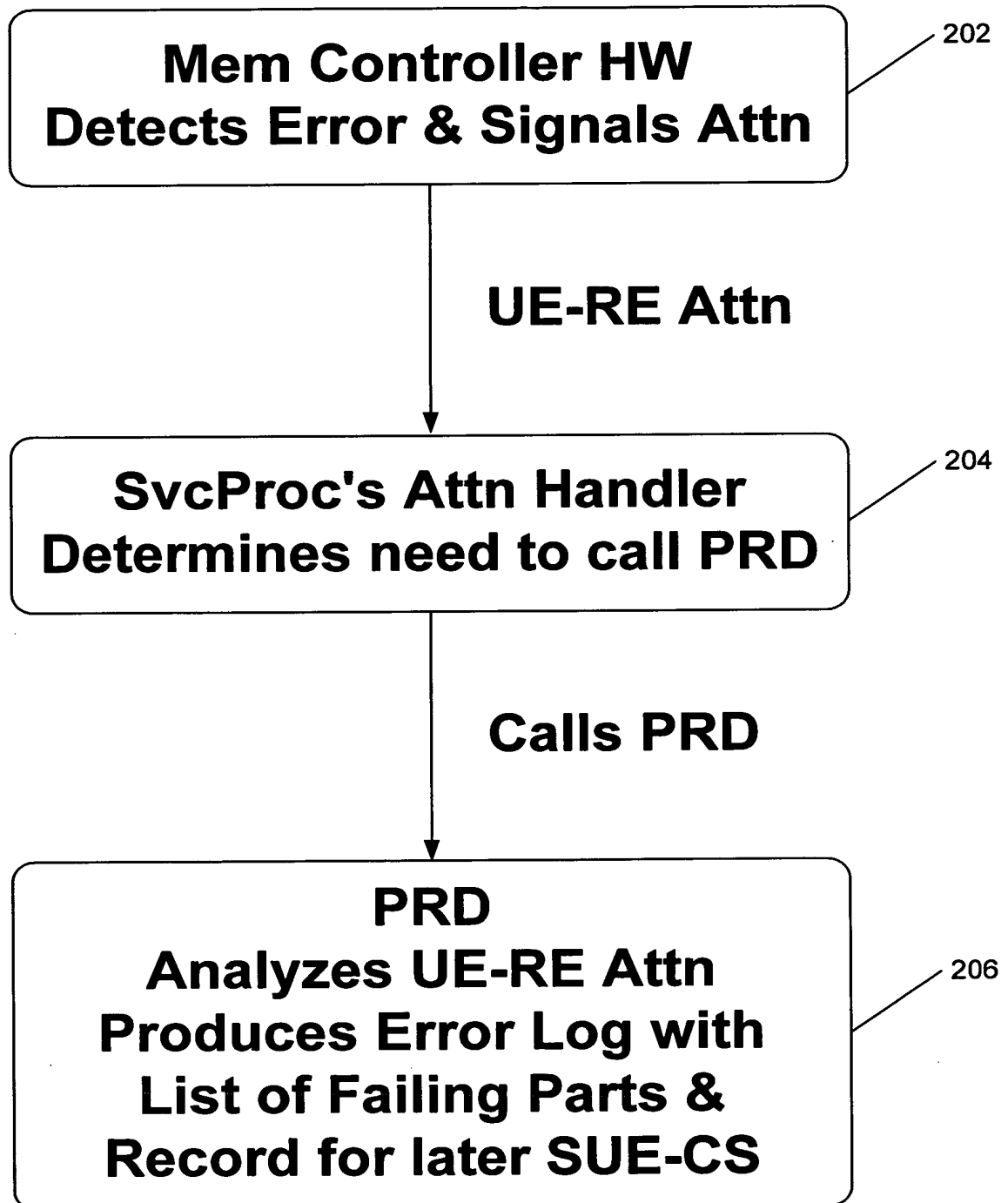


FIG. 4



09/820,461

5/7

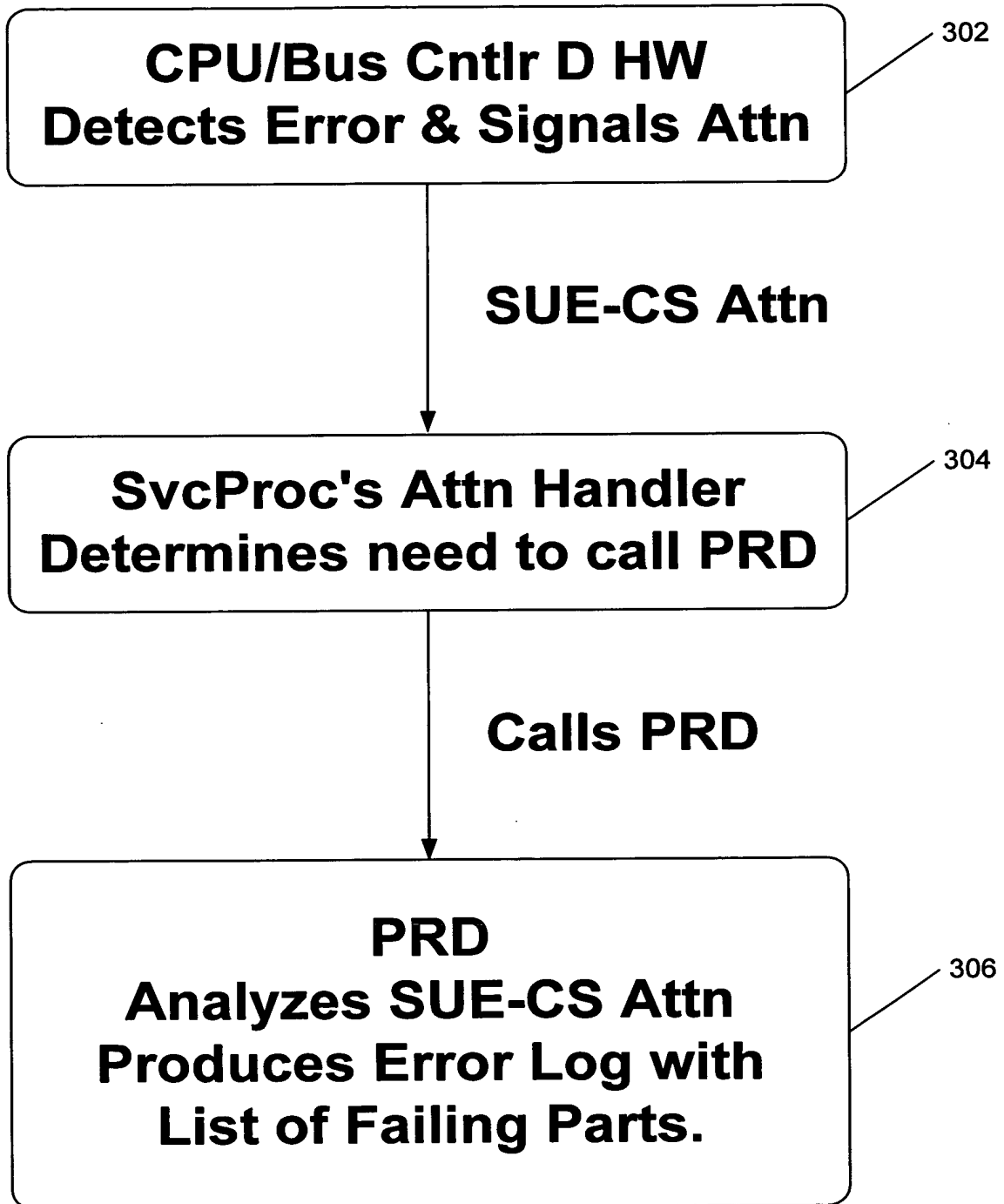


FIG. 5

6/7

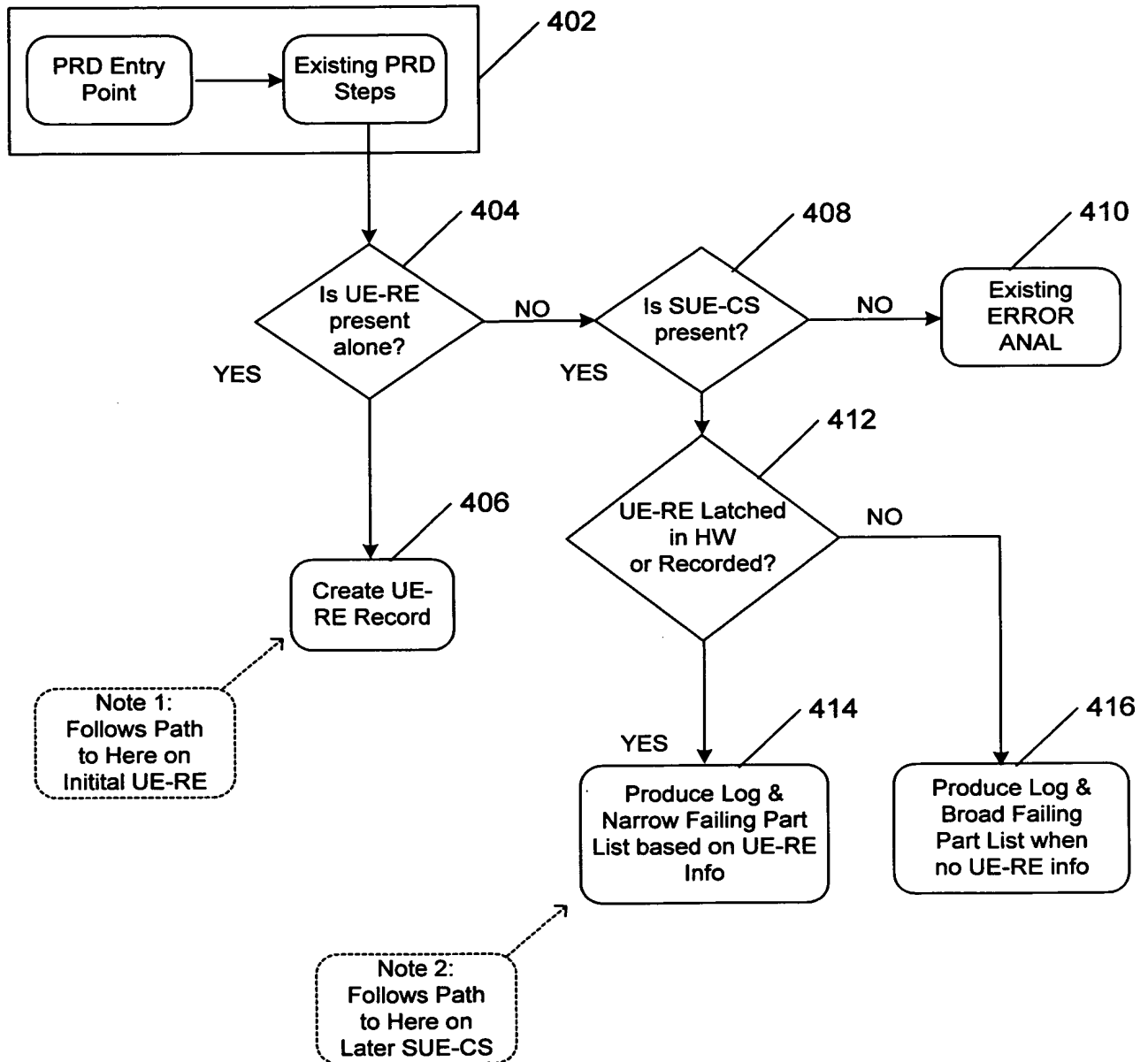


FIG. 6

7/7

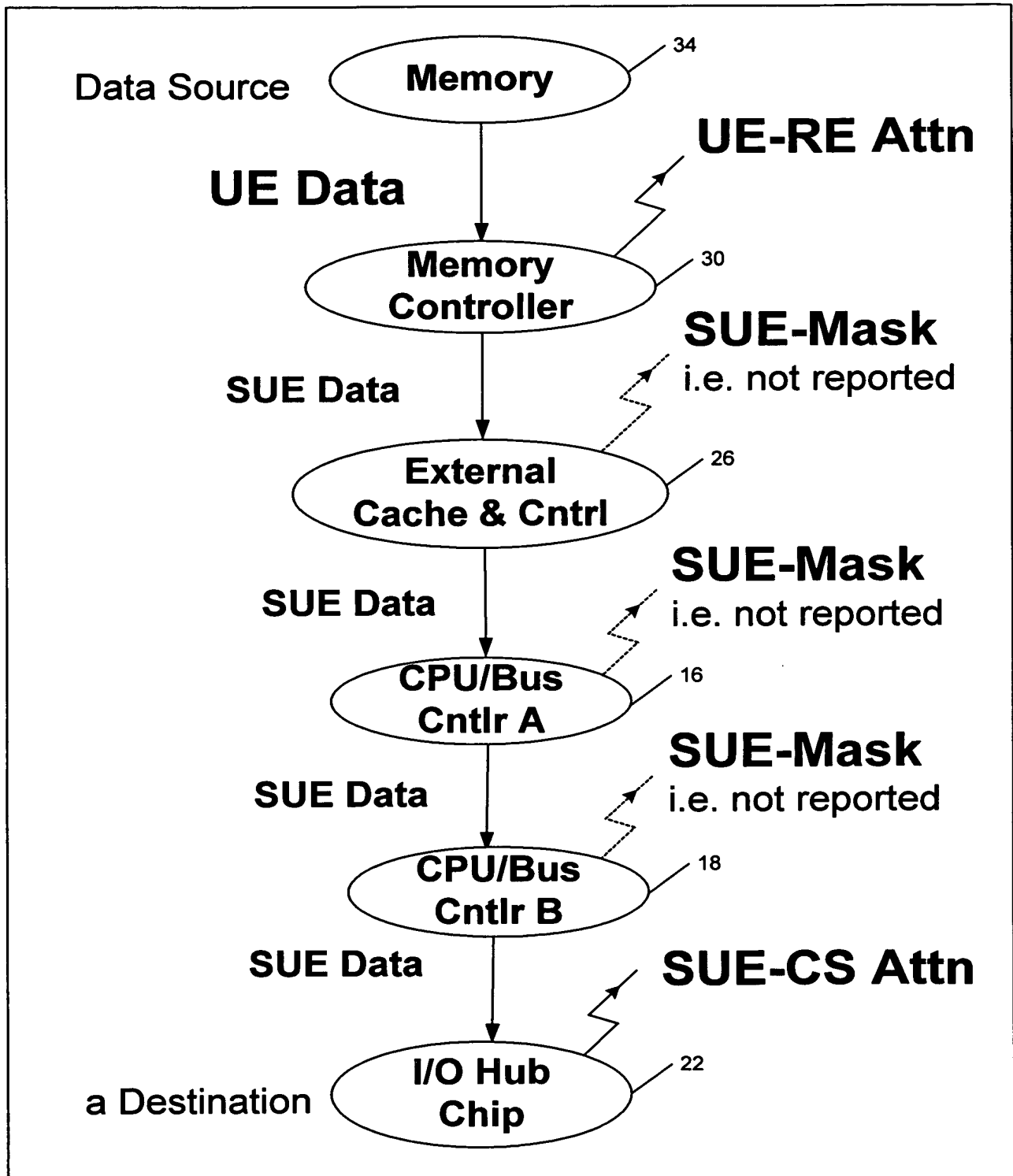


FIG. 7